

INFORMATION DISCLOSURE  
CITATION

ATTY. DOCKET NO.

SERIAL NO.

550-241

09/887,522

APPLICANT

NEVILL et al.

FILING DATE

TC/AU.

June 25, 2001

2183



(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AL	5,805,895	9/1998	Breternitz, Jr.			
AL	3,955,180	5/1976	Hirtle			
AL	5,970,242	10/1999	O'Connor			
AL	5,619,665	4/1997	Emma			
AL	5,826,089	10/1998	Ireton			
AL	5,953,520	9/1999	Mallick			
AL	5,568,646	10/1996	Jaggar			
AL	5,758,115	5/1998	Nevill			
AL	5,367,685	11/1994	Gosling			

## FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO

## OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

AL	IBM Technical Disclosure Bulletin, March 1988, pp308-309, "System/370 Emulator Assist Processor For a Reduced Instruction Set Computer".
AL	IBM Technical Disclosure Bulletin, July 1986, pp548-549, "Full Function Series/1 Instruction Set Emulator".
AL	IBM Technical Disclosure Bulletin, March 1994, pp605-606, "Real-Time CISC Architecture HW Emulator On A RISC Processor".
AL	IBM Technical Disclosure Bulletin, March 1998, p272, "Performance Improvement Using An EMULATION Control Block".
AL	IBM Technical Disclosure Bulletin, January 1995, pp537-540, "Fast Instruction Decode For Code Emulation on Reduced Instruction Set Computer/Cycles Systems".
AL	IBM Technical Disclosure Bulletin, February 1993, pp231-234, "High Performance Dual Architecture Processor".
AL	IBM Technical Disclosure Bulletin, August 1989, pp40-43, "System/370 I/O Channel Program Channel Command Word Prefetch".
AL	IBM Technical Disclosure Bulletin, June 1985, pp305-306, "Fully Microcode-Controlled Emulation Architecture".
AL	IBM Technical Disclosure Bulletin, March 1972, pp3074-3076, "Op Code and Status Handling For Emulation".
AL	IBM Technical Disclosure Bulletin, August 1982, pp954-956, "On-Chip Microcoding of a Microprocessor With Most Frequently Used Instructions of Large System and Primitives Suitable for Coding Remaining Instructions".

\*Examiner

Shirley Li

Date Considered

12-20-2004

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<i>ST</i>	IBM Technical Disclosure Bulletin, April 1983, pp5576-5577, "Emulation Instruction"
<i>ST</i>	ARM System Architecture by S Furber.
<i>ST</i>	Computer Architecture: A Quantitative Approach by Hennessy and Patterson; and the book The Java Virtual Machine Specification by Tim Lindholm and Frank Yellin 1st and 2nd Editions.
<i>ST</i>	The Java Virtual Machine Specification, Tim Lindholm.

\*Examiner

*James Li*

Date Considered

*12-20-2004*

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